

**NITT/EEE/DST-SUSTENANCE/2021**

**12<sup>th</sup> August 2021**

**ADVERTISEMENT**

Applications are invited from eligible candidates for the position of **Junior Research Fellow (JRF)** for the DST, GOI project titled “**Sustainable Energy system for Achieving Novel Carbon neutral Energy communities (SUSTENANCE)**” in the Department of Electrical and Electronics Engineering, Department of Computer Science and Engineering, National Institute of Technology, Tiruchirappalli.

Position & Number	Junior Research Fellow (JRF) – 2 Nos.
Essential qualification	B.E/B.Tech in Electrical/CSE with M.E/M.Tech in Power Electronics/Power systems/CSE. Minimum 70% or 7.5CGPA in UG and PG from a recognized University/ Institute. Age should be less than 30 years and should have valid GATE Score
Job Description	1. Purchase of Equipment and inventories for the project. 2. Design and development of converters for PV Applications. 3. Site Survey. 4. Design and development of energy management software. 5. The candidate selected as Junior Research Fellow, will be permitted to register for PhD programme as per NITT rules and regulations. 6. The candidate selected as JRF will get an opportunity to work with IIT Bombay, IISc Bangalore, European countries.
Salary	Rs. 31000 +16% HRA
Duration of Project	42 Months (extension based on performance review every 12 months)

- Eligible candidates may send the scanned copy of their duly filled applications (only in the prescribed format given below) along with the relevant supporting documents by email to: [gsi.stc.nitt@gmail.com](mailto:gsi.stc.nitt@gmail.com) with the subject marked as “**JRF Application-DST-SUSTENANCE**”.
- Preference will be given to the candidate who is having good academic record, research potential, relevant experience. However, the selection of the candidate would be based on their performance in the Written Test and Interview.
- The date of written test and interview will be communicated to the shortlisted candidates by **E-mail**. No T.A/D.A will be paid for attending the written test and the interview.
- The selection committee decision is final. If any applicant is found to canvass in any manner, the candidate selection shall be summarily rejected.
- The last date for receipt of applications is **10.09.2021**

**Dr. G. Saravana Ilango (Principal Investigator)**

Associate Professor, Department of Electrical and Electronics Engineering  
National Institute of Technology, Tiruchirappalli – 620015.

**OFFICE OF THE DEAN (R & C), NIT, TIRUCHI**  
**RECRUITMENT OF TEMPORARY PROJECT STAFF - APPLICATION FOR THE**  
**POST OF RESEARCH ASSOCIATE / JRF**

<p style="text-align: center;">For Office Use only</p> <p>Serial Number allotted to the Applicant .....</p> <p style="text-align: center;">Signature of the Application Screening Committee Chairman</p>	<p style="text-align: center;">Paste here your recent passport size color Photograph</p>
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**APPLICATION FOR THE POST OF RESEARCH ASSISTANT / JRF**

1. Name of the Candidate in full					
2. Nationality					
3. Mother Tongue					
4. Date of Birth	Date	Month	Year		
5. Sex	Male / Female				
6. Marital Status	Married / Single				
7. Category SC / ST / OBC / General	Please specify				
8. Address for communication with Mobile number and E Mail ID					
9. Project Title					
10. Name of the Post applied for					
11. Advertisement Number with Date					
Academic Career : Enclose photo copies of the qualifying degree certificates and mark sheets/ grade cards showing the percentage of marks or CGPF from HSC onwards					
Name of Exam	School / University	Board/ University	Broad subjects of study	Year of Passing	% of marks / GCPA
S.S.L.C					
H.S.C					
Graduation					
Post-Graduation					
PhD					

**Enclose self-attested Xerox copies**

13. GATE Score..... Subject  
..... Year.....

14. NET Score  
..... Subject..... Year.....  
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15. Work Experience, if any

Organization	Designation	Duration		Nature of Job
		From	To	

16. Previous Research work and publication, if any (enclose copies of published or unpublished work)

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17. Proof of past study as a sponsored candidate, or other contractual obligation, if any:

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18. Reference

Name	Address	Phone & E Mail ID

19. I do not have a relative among Faculty and Staff of the NIT, Trichy.

20. Declaration: I do hereby that the information furnished in the application is true to the best of my knowledge and belief. If admitted, I shall abide by the rules and regulations of the Institute and Project associated to it. If any information furnished in this application is found to be untrue, I am liable to forfeit the seat / appointment allotted to me any time in future and legal action be taken against me.

Date:

Full signature of the Applicant

List of Enclosures: (1)  
(2)  
(3)  
(4)