



(An Institution of National Importance under MHRD, Government of India)  
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No:IIITDMK/PR/LE/1/2021

09 February 2021

### **Advertisement for Lab Engineer Position in Sponsored Project**

Indian Institute of Information Technology Design and Manufacturing, Kancheepuram, Chennai is an Institution of National Importance established in 2007 by the Ministry of Human Resource Development, Government of India and is located at outskirts of Chennai. The institute is at a distance of 25 KM from Chennai international airport, 15 KM from Tambaram railway station and 40KM from Chennai central. It is a Centre of Excellence for pursuing Design and Manufacturing Oriented Engineering Education and Research for promoting competitive advantage of Indian products in global markets.

IIITDM Kancheepuram, Chennai is presently offering undergraduate, postgraduate, dual-degree programs in the areas of Computer, Electronics, Mechanical Engineering and inter-disciplinary Ph.D programs in core and applied areas of basic sciences and engineering. It functions with the vision to groom engineers with design and manufacturing skills. The institute gives significant amount of emphasis for practice courses and theory concepts are also explored along with the relevant laboratory course. IIITDM Kancheepuram invites online application for the following project positions:

<b>Name of the Research Project</b>	“SMDP C2SD”
<b>Duration of the project</b>	5 years (60 Months) ending on 30 <sup>th</sup> November 2021
<b>Name of the principal Investigator / Co PI</b>	Dr. Noor Mahammad (noor@iiitdm.ac.in), Phone--+91 44 2747 6349 Dr. Binsu J Kailath (bkailath2iiitdm.ac.in), Phone--+91 44 2747 6341
<b>Funding Agency</b>	Ministry of Electronics and Information Technology (MeitY)
<b>Name of the Project Position</b>	Lab Engineer
<b>Number of Positions</b>	1 Position
<b>Qualification</b>	<b>Essential:</b> B.Tech. (ECE), M.Tech. in VLSI Design with minimum 6.5 CGPA (or 60% marks) <b>Desirable:</b> Expertise in field of Physical Design, VLSI/related area, working knowledge in VLSI tools like Cadence / Synopsys/ Mentor/ Xilinx, etc., publication in reputed Journals.
<b>Age</b>	Below 28 Years
<b>Stipend</b>	34000/-pm
<b>Other Important Point</b>	The position will be available up to 30 <sup>th</sup> November 2021.

**General Conditions/Instructions for all posts:**

1. Only Indian Nationals need to apply.
2. The candidates are required to work for the success of the project.
3. Candidates should bring self-attested copies of the relevant certificates/testimonials along with the original certificates/documents at the time of interview.
4. No travelling or any other allowances is admissible for attending the interview.
5. The Institute reserves the right not to fill up the position, if it so decides.
6. The Institute reserves the right to terminate the appointment at any time before completion of the period, if it so decides.
7. No interim correspondence will be entertained.
8. Canvassing in any form will lead to disqualification.
9. Last date for submission of online application is **28<sup>th</sup> February 2021**.

**Syllabus:-**

**TEST:**

**MOSFET based amplifiers, OP-AMP circuits, CMOS Analog Integrated Circuits, Combinational Circuit Design, Sequential Circuit Design, CMOS Logic Design**

**Duration: 1 Hour**

**Exam Type - Objective Type Problem based 10 Questions for 20 Marks.**

Last date for submission of online application: **28<sup>th</sup> February 2021**.

Tentative date for written test and interview on: **10<sup>th</sup> March 2021**.

**Place of Interview: Online**

Contact No: 044-27476393

Email: sricce@iiitdm.ac.in

Sd/xx

Dean (SR)

IIITDM Kancheepuram